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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: Akira YAMANOUE et al.

Group Art Unit: 2823

Serial No.: 10/673,354

Examiner: Toledo, Fernando L.

Filed: September 30, 2003

Confirmation No.: 4037

For: **METHOD FOR FABRICATING SEMICONDUCTOR DEVICE HAVING
GATE ELECTRODE TOGETHER WITH RESISTANCE ELEMENT (as
amended).**

Attorney Docket No.: 031736

Customer Number: 38834

RESPONSE UNDER 37 C.F.R. §1.116

- EXPEDITED RESPONSE -

Mailstop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

*Do not enter
2/14/05*

February 1, 2005

Sir:

In response to the Office Action dated November 1, 2004, Applicants amend the application as follows and submits the following remarks.

Amendments to the Claims are reflected in the listing of claims that begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.